EAST Search History

EAST Search History (Prior Art)

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S34	9	S33 AND (simulat\$3 emulat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/30 14:55
\$33	32	S32 AND (switch near fabric) AND ((chip adj set) chip\$1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/30 14:54
S32	450	payload AND (cell WITH (size sized sizing)) AND ((rhip micro sized sizing)) AND ((rhip micro sized sized sizing)) AND ((rhip micro sized s	US-PGPUB; USPAT; USPAT; USPOR; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR.	ON	2010/07/30 14:53
S31	11	\$24 not \$25	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:20
\$30	10	S29 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	OR	NON	2010/07/29 23:05

S29	10	\$28 not \$24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:05
S28	11	\$27 AND ((chip micro \$1 processor" ASC "application specific integrated circuit" application \$1 specific integrated circuit" application \$1 specific integrated circuit IC "integrated circuit to \$1 processor" corposessor "corposessor "corposessor "corposessor "corposessor "corposessor "corposessor" with (version versioning revision revisioning revision revisioning to identification identify identified dentifying tag tagged tagging Mark Marked Marking label labeled labelled labelling name named [anaming model])	US PGPUB; USPAT; USPAT; USPAT; EPO, JPO; DEWENT; IBM_TDB	OR	ON	2010/07/29 23:05
S27	58	emulat\$3 AND (switch near fabric) AND ((chip adj set) chip \$1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:04
S26	0	emulat\$3 SAME (switch near fabric) SAME ((chip adj set) chip \$1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	00	2010/07/29 23:04
S25	19	\$24 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03

524		S22 AND ((chip micro \$1 processor" ASC "application specific integrated circuit" application \$1 specific integrated\$ circuit of \$1 specific\$ integrated\$ circuit of \$1 specific\$ integrated\$ circuit of \$1 processor\$ (integrated\$ circuit of \$1 processor\$ (integrated\$ adj circuit) (digital adj signal adj processor\$) with (version versioning revision revision ingerision tervisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labelled labelled naming model))	US-PGPUB; USPAT; USPOT; FFRS; EPC, JPC; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03
S23	94	SSZ AND (chip micro \$1 processor "micro processor" ASI C "application specific integrated circuit" application \$1 specific\$ integrated\$1 circuit IC "integrated circuit" integrated\$1 circuit co \$1 processor" (integrated adj circuit) (digital adj signal adj processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03
S22	95	simulat\$3 AND (switch near fabric) AND ((chip adj set) chip \$1 set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:02
S21	1	simulat\$3 SAME (switch near fabric) SAME ((chip adj set) chip \$1 set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:02
	8	St 6 AND ((chip micro \$1 processor" hicro processor" AS C "application specific integrated circuit" application \$1 specifics' integrated \$1 circuit IC "integrated circuit" integrated \$1 circuit C "integrated circuit" integrated \$1 circuit \$1 processor "co processor" co processor "co processor" with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labeled labeleing name named inaming model))	US PGPUB; USPAT; USCOR; FPRS; EPC, JPC; DEBWENT; IBM_TDB	OR	ON	2010/07/29 22:53

S19	12	S16 AND (chip micro \$1 processor" micro processor' AS C"application specific integrated circuit" application \$1 specifics integrated\$1 circuit IC "integrated\$1 circuit IC "integrated\$1 circuit or processor" (integrated adj circuit) (digital adj signal adj processor))	US-PGPUB; USPAT; USOCR; FFRS; EFO; JFO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:52
S18	1	S16 AND (cell WITH (size sized sizing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:52
S17	3	S16 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:43
S16	19	(US-20030038442-8 or US-20020188245-5 or US-20020188245-5 or US-200400287512-3 or US-2004002842-8 or US-20040102842-3 or US-20040102843-3 or US-20040028578-3 or US-20040024578-3 or US-2005017735-3 or US-6082361-3 or US-608398-3 or US-7085703-3 or US-64462898-3 or US-7085703-3 or US-6442740-3), did.	US-PGPUB; USPAT	OR	ON .	2010/07/29 22:43

EAST Search History (Interference)

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	0	12 AND (simulat\$3 emulat\$3). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:50
L12	12	11 not 9	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:26
L11	12	8 not 1	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:25

L10	0	7 AND ((chip adj set) chip\$1set). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:25
L9	2	7 AND (simulat\$3 emulat\$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:23
L8	13	7 AND (switch near fabric).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:22
L7	111	payload.clm, AND (cell WITH (size sized sizing)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:22
L6	0	4 AND ((chip adj set) chip\$1set). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L5	0	4 AND (switch near fabric).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L4	241	payload.clm. AND (simulat\$3 emulat\$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L3	0	payload.clm. AND (switch near fabric).clm. AND ((chip adj set) chip\$1set).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L2	1	(simulat\$3 emulat\$3).clm. AND (switch near fabric).clm. AND ((chip adj set) chip\$1set).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:18
L1	3	payload.clm. AND (cell WITH (size sized sizing)).clm. AND ((chip micro\$t processor "micro processor" ASIC "application specific integrated circuit" application\$t specific\$t integrated \$t\$ circuit ("integrated sicrouit" integrated\$t circuit co\$t processor" (integrated adj circuit) (digital adj signal adj processor)).clm. with (version versioning revision revisioning batch ID identification identify identifying tag tagged tagging Mark Marked Marking label tabled labelled labelling labelling name named naming model).clm.	US-PGPUB; USPAT; UPAD	OFI.	ON.	2010/07/30 22:18

7/ 30/ 2010 10:51:15 PM

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